Seeking a qualified candidate for a post-doctoral position in semiconductor packaging and test.

Position: Postdoctoral Scholar, 1.0 FTE
Location: Corvallis, Oregon
Duration: 1 years(September 16, 2025 to September 15, 2026; start date flexible, renewable for a second year based on satisfactory performance)
Availability: Immediately, open until filled
Salary: Commensurate with qualifications and experience as per OSU guidelines

Position Summary: This position as a Postdoctoral Scholar is a fixed-term, 12-month, 1.0 FTE appointment through the School of Electrical and Computer Engineering (ECE) with the Tadayon Research Group.

Description of Duties:

1. Create an individual development plan to establish career and project-based research (and optional teaching) goals and track progress towards achieving stated goals.

2. Participate in starting up a new research lab targeted at semiconductor packaging and test. Duties will include identifying research opportunities, defining goals and objectives, laying out experimental plan, identifying/procuring/installing equipment and instruments, and establishing the infrastructure to carry out long term research projects.

3. Drive execution of experiments and research activities, including data analysis, presentation to peers, and manuscript preparation for publication in peer reviewed journals. Travel may be required to collect data and present research to scientific peers.

4. Identify and collaborate with ecosystem/industry partners to advance the technology and leverage partner capabilities to accelerate research goals.

5. Assist with the development of research proposals; participate in a variety of outreach activities; and support undergraduate and graduate student success.

6. Additional professional development opportunities include:
    a. Establishing collaborations with project personnel, graduate students, OSU faculty, and collaborating faculty at other institutions.

b. Supervision and mentorship of graduate students; with input from the project PI, the postdoctoral scholar will plan, assign, and approve work.
c. Professional networking and soft skills development by participating in conferences, workshops, seminars, and training programs, with supervisor approval.
d. Project report and grant writing experience, with supervisor approval.
e. If interested, periodically lead lectures and/or recitations for semiconductor manufacturing courses.

Work Schedule/Working Conditions: Full time. Work will generally be conducted in a laboratory, field, or office setting. Occasionally projects may require the postdoctoral scholar to work at atypical times (e.g., evenings, weekends). Travel may be required to collect unique scientific data and present research to scientific peers.

Minimum Required Qualifications:

• Ph.D. in in relevant engineering disciplines (electrical engineering, mechanical engineering, materials science, chemistry, physics, or similar).
• Proven track-record of independent research, critical thinking, and successful academic publications.
• Excellent written and verbal communication skills.
• Experience in semiconductor processing, especially in semiconductor advanced packaging or test. Experience with lithography and deposition techniques is highly desirable.
• Experience in writing manuscripts for peer-reviewed publications
• Ability to work independently and in a team
• Excellent organizational and time management skills

Preferred Special Qualifications:

• Education, training or prior experience with semiconductor manufacturing; specifically advanced packaging and/or test.
• Education, training or prior experience with extensive hands-on processing techniques including lithography, electrodeposition, CVD/PVD, CMP. Experience with characterization techniques such as scanning electron microscopy (SEM), transmission electron microscopy (TEM), 3D X-ray, and acoustic microscopy.
• Education, training or prior experience with single and two phase thermal management of semiconductors.

Other Job-Related Skills and Abilities: A criminal history check will be required.

Please note: only qualified candidates with the required expertise will be contacted. Applicants with experience in semiconductor processing, advanced packaging, semiconductor testing are encouraged to apply.

Application Materials:

- A detailed CV.
- A two-page statement describing your background and how you fit the advertised position.
- Contact information for three references.

To apply: Submit application materials as a single .pdf file via email (subject line: "PD application") to Professor Pooya Tadayon at tadayonp@oregonstate.edu.

Contact:

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